



EUROPEAN TRAINING SCHEDULE FOR EMEA JULY – DECEMBER 2007

CIC COURSES						
Class	Bu	Days	Bracknell,UK/ Kista, Sweden	Velizy, France	Munich, Germany	Herzelia, Israel
Behavioral Modeling with Verilog-AMS	CIC	3	1-3 Oct	On Request	On Request	On Request
Skill Programming for IC Layout Design	CIC	2	2-3 Aug	On Request	On Request	On Request
Analog M.S.: Virtuoso Platform Update	CIC	1+3	6-9 Aug	10 Sep, 12-14 Sep, 10-13 Dec	9-12 Jul, 28-31 Aug, 26-29 Nov	25-29 Nov
Infrastructure: Virtuoso Platform Update	CIC	2	23-24 Aug	10-11 Sep, 10-11 Dec	20-21 Aug, 19-20 Nov	5-6 Dec
Analog Modeling with Verilog® -A	CIC	3	17-19 Sep	15-17 Oct	4-6 Jul, 17-19 Sep, 15-17 Oct	30 Oct-1 Nov
Simulation and Analysis using OCEAN	CIC	2	3-4 Sep	On Request	On Request	25-26 Jul
Virtuoso AMS Designer	CIC	3	13-15 Aug, 26-28 Nov	19-21 Sep, 22-24 Oct, 3-5 Dec	16-18 Jul, 3-5 Sep, 22-24 Oct, 10-12 Dec	On Request
Virtuoso Analog Design Environment	CIC	4	16-19 Jul	4-7 Sep, 12-15 Nov	31 Jul-3 Aug, 18-21 Sep, 6-9 Nov, 18-21 Dec	On Request
Virtuoso Analog Simulation Techniques V6.1	CIC	3	20-22 Aug	3-5 Dec	10-12 Oct, 5-7 Dec	On Request
Virtuoso Design Entry and Basic Simulation V6.1	CIC	2	26-27 Jul	6-7 Dec	25-26 Oct, 13-14 Dec	On Request
Virtuoso NeoCircuit Circuit Sizing and Optimization	CIC	2	6-7 Aug	On Request	On Request	On Request
Virtuoso Spectre® Circuit Simulator	CIC	3	6-8 Aug	24-26 Sep	23-25 Jul, 26-28 Sep, 14-16 Nov	On Request
Virtuoso Spectre® RF Tools	CIC	4	1-4 Oct, 17-20 Dec	19-22 Nov	7-10 Aug, 26-29 Nov	8-11 Oct
Virtuoso UltraSim Full-Chip Simulator	CIC	3	23-24 Jul	4-5 Oct	On Request	On Request
Skill Development of Parameterized Cells	CIC	2	16-17 Aug	On Request	9-10 Aug, 24-25 Sep, 12-13 Nov	On Request
Skill Language Programming	CIC	5	13-17 Aug, 26-29 Nov	17-21 Sep, 5-9 Nov	23-27 Jul, 10-14 Sep, 5-9 Nov, 17-21 Dec	16-20 Sep
Abstract Generation	CIC	1	15-Aug	On Request	On Request	13-Dec
Virtuoso Chip Assembly Router (VCAR)	CIC	3	17-19 Jul	8-10 Oct, 19-21 Dec	30 Jul-1 Aug, 28-30 Nov	On Request
Virtuoso Chip Editor	CIC	2	26-27 Jul	On Request	On Request	On Request
Virtuoso Layout Design Basics V6.1	CIC	1	03-Aug	28-Nov	29-Oct	On Request
Virtuoso Layout Editor	CIC	2	10-11 Sep	27-28 Sept, 29-30 Nov	16-17 Jul 24-25 Sep, 22-23 Oct, 10-11 Dec	14-16 Aug
Virtuoso Layout Migrate	CIC	2	27-29 Nov	On Request	On Request	On Request
Virtuoso NeoCell Analog Physical Synthesis	CIC	2	19-20 Jul	On Request	On Request	On Request
Virtuoso Schematic Editor	CIC	2	22-23 Oct	On Request	31 Jul-1 Aug, 18-19 Sep, 6-7 Nov, 18-19 Dec	On Request

CIC COURSES						
Class	Bu	Days	Bracknell,UK/ Kista, Sweden	Velizy, France	Munich, Germany	Herzelia, Israel
Virtuoso-XL Layout Editor	CIC	3	4-6 Sep	1-3 Oct, 27-29 Nov	18-20 Jul, 26-28 Sep, 24-26 Oct, 12-14 Dec	On Request
Virtuoso Aptivia Specification-Driven Environment	CIC	2	On Request	On Request	13-14 Aug, 29-30 Oct	1-2 Aug
Cadence Design Framework II	CIC	3	On Request	On Request	20-22 Aug, 17-19 Oct	On Request

SPB COURSES						
Class	Bu	Days	Bracknell,UK/ Kista, Sweden	Velizy, France	Munich, Germany	Herzelia, Israel
Allegro AMS Simulator Advanced Analysis	SPB	3	22-24 Oct	On Request	8-10 Aug, 26-28 Sep, 6-8 Nov, 18-20 Dec	
Allegro Design Reuse	SPB	1	10-Oct	1 Oct, 10 Dec	10 Aug, 26 Oct	
Allegro High Speed Constraint Management	SPB	1	14-Sep	2 Oct, 11 Dec	9 Oct, 11 Dec	
Allegro Package Co-Design	SPB	1	26-Jul	7 Sep, 9 Nov	7 Sep, 9 Nov	
Allegro PCB Editor Advanced Techniques	SPB	2	1-2 Oct	On Request	7-8 Aug, 13-14 Dec	
Allegro PCB Router Advanced Techniques	SPB	2	3-4 Oct	On Request	9-10 Aug, 8-9 Nov	
Allegro PCB SI GXL	SPB	2	25-26 Jul	On Request	30-31 Aug, 29-30 Nov	
Allegro Design Workbench: for Administrators	SPB	2	19-20 Nov	On Request	On Request	
Allegro Design Workbench: for Engineers and Designers	SPB	1	21-Nov	On Request	On Request	
Allegro Design Workbench: for Librarians	SPB	1	22-Nov	On Request	On Request	
Allegro AMS Simulator	SPB	3	24-26 Sep	5-7 Sept	On Request	
Allegro Design Entry HDL (Concept)	SPB	3	3-5 Sep, 26-28 Nov	17-19 Sep, 15-17 Oct, 26-28 Nov	9-11 Jul, 20-22 Aug, 8-10 Oct, 19-21 Nov	
Allegro PCB Librarian	SPB	2	4-5 Oct	20-21 Sep, 18-19 Oct, 29-30 Nov	12-13 Jul, 23-24 Aug, 11-12 Oct, 22-23 Nov	
Analog Simulation with PSpice	SPB	3	8-10 Oct	22-24 Oct	1-3 Aug, 10-12 Sep, 22-24 Oct, 10-12 Dec	
OrCAD Capture	SPB	2	23-24 Jul, 5-6 Nov	On Request	16-17 Jul, 3-4 Sep, 15-16 Oct, 3-4 Dec	
Advanced OrCAD Layout	SPB	2	26-27 Jul, 8-9 Nov	On Request	On Request	
Allegro PCB Automatic Placement Editor	SPB	1	08-Aug	On Request	6 Aug, 5 Nov	
Allegro PCB Designer	SPB	2	20-22 Aug	On Request	16-18 Jul, 27-29 Aug, 15-17 Oct, 26-28 Nov	
Allegro PCB Editor	SPB	5	6-10 Jul, 10-14 Dec	24-28 Sep, 5-9 Nov, 3-7 Dec	16-20 Jul, 27-31 Aug, 15-19 Oct, 26-30 Nov	
Allegro PCB Router	SPB	2	6-7 Aug	12-13 Sep, 21-22 Nov	7-8 Aug 6-7 Nov	
OrCAD Layout	SPB	3	23-25 Jul, 5-7 Nov	On Request	18-20 Jul, 5-7 Sep, 17-19 Oct, 5-7 Dec	
Allegro Design Entry HDL SI	SPB	3	5-7 Nov	On Request	On Request	

SPB COURSES						
Class	Bu	Days	Bracknell,UK/ Kista, Sweden	Velizy, France	Munich, Germany	Herzelia, Israel
Allegro PCB Power Integrity	SPB	1	28-Sep	On Request	On Request	
Allegro PCB SI EMControl	SPB	1	26-Sep	On Request	On Request	
Allegro PCB SI Foundations	SPB	3	20-22 Nov	3-5 Oct, 12-14 Dec	26-28 Sep, 26-28 Nov	
Allegro PCB SI Model Integrity	SPB	1	27-Sep	On Request	On Request	
Allegro System Architect	SPB	2	10-11 Sep	On Request	On Request	
Essential High-speed PCB Design for Signal Integrity + EMI	SPB	5	13-17 Aug	12-16 Nov	17-19 Sep 12-14 Nov	
Allegro Package Designer 620 Introduction	SPB	1	17-Sep	On Request	6 Sep, 8 Nov	
Allegro Package Designer	SPB	4	18-21 Sep	On Request	21-24 Sep, 23-26 Oct	
SiP Digital Layout	SPB	5	12-16 Nov	On Request	24-28 Sep, 10-14 Dec	
SiP Digital SI	SPB	3	19-21 Nov	On Request	3-5 Sep, 29-31 Oct	
SiP RF Architect	SPB	2	22-23 Nov	On Request	20-21 Aug, 1-2 Oct	
Allegro PCB Editor and Design Entry HDL SKILL Programming Language	SPB	5	30 Jul-3 Aug	On Request	10-14 Sep	
SPB Update 15.2 (Concept and Editor)	SPB	2		10-11 Sept, 19-20 Nov		
SPB Update 15.5 (Concept and Editor)	SPB	1		8 Oct, 17 Dec		
SPB Update 15.7 (Concept and Editor)	SPB	1		9 Oct, 18 Dec		
SPB Update 16.0 (Concept and Editor)	SPB	2		10-11 Oct, 19-20 Dec		

ICD COURSES						
Class	Bu	Days	Bracknell,UK/ Kista, Sweden	Velizy, France	Munich, Germany	Herzelia, Israel
CeltIC Nanometer Delay Calculator (NDC)	ICD	2	6-7 Dec	On request	6-7 Sep, 22-23 Nov	On request
Encounter Flow Management for Developers	ICD	1	22-Oct	On Request	28 Aug, 13 Nov	On request
Low-Power Implementation	ICD	2	22-23 Aug	15-16 Nov	25-26 Jul, 20-21 Sep, 29-30 Oct, 13-14 Dec	On request
Signoff Timing Analysis with Encounter Timing System	ICD	2	23-24 Oct	On Request	On Request	14-15 Nov
Encounter Flow Management System	ICD	1	14-Sep	On Request	27 Aug, 12 Nov	On request
SoC Encounter XL RTL-to-GDSII Hierarchical Flow	ICD	2	10-11 Sep	On Request	On Request	On request
Virtuoso Digital Implementation Option	ICD	2	12-13 Sep	On Request	9-11 Jul, 10-12 Sep, 22-24 Oct, 3-5 Dec	On request
Advanced Logic Equivalence Checking with Conformal EC	ICD	1	06-Sep	On Request	10-Oct	On request
Custom Equivalence Checking with Encounter Conformal	ICD	1	05-Sep	On Request	09-Oct	On request
Encounter Conformal Constraint Designer	ICD	1	03-Sep	On Request	On Request	On request
Logic Equivalence Checking with Encounter Conformal EC	ICD	1	04-Sep	On Request	08-Oct	On request
Detail Routing with NanoRoute Ultra	ICD	2	13-14 Aug	26-27 Nov	11-12 Jul, 19-20 Sep, 24-25 Oct, 5-6 Dec	On request
Encounter RTL Compiler	ICD	2	24-25 Sep	25th-26th Oct	19-20 Jul, 6-7 Sep, 15-16 Oct, 10-11 Dec	10-11 Oct
Floorplanning and Physical Synthesis with First Encounter XL	ICD	2	30-31 Jul	18-19 Oct	9-10 Jul, 17-18 Sep, 22-23 Oct, 3-4 Dec	On request

FV COURSES							
Class	Bu	Days	Bracknell,UK/ Kista, Sweden	Velizy, France	Munich, Germany	Herzelia, Israel	
Advanced PSL and Formal Verification	FV	1	26-Nov	12 Sep, 5 Dec	14 Sep, 23 Nov	On Request	
Advanced SVA and Formal Verification	FV	1	05-Oct	On Request	On Request	On Request	
SystemC Transaction Level Modeling	FV	1	17-Dec	On Request	On Request	On Request	
SystemC Verification Libraries (SCV, VIC)	FV	3	27-29 Nov	On Request	On Request	On Request	
Verification with VHDL	FV	4	20-23 Aug	On Request	On Request	On Request	
Introduction to PERL	FV	3	7-9 Nov	On Request	6-8 Aug, 17-19 Sep, 29-31 Oct, 17-19 Dec	4-6 Sep	
PERL Programming for Professionals	FV	2	5-6 Nov	On Request	9-10 Aug, 20-21 Sep, 20-21 Dec	On Request	
Tcl Scripting for EDA + Intro to Tk	FV	2+1	23-25 Oct	On Request	2-3 Aug, 8-9 Oct	16-18 Jul	
C++ Introduction for SystemC Users	FV	2	19-20 Nov	24-25 Sep, 12-13 Nov	16-17 Jul, 15-16 Oct	On Request	
SystemC 2.1 Fundamentals	FV	3	21-23 Nov	26-28 Sep, 14-16 Nov	18-20 Jul, 17-19 Oct	23-25 Jul	
SystemVerilog Assertions (SVA)	FV	2	1-2 Oct	6-7 Dec	13-14 Aug, 19-20 Nov	19-20 Aug	
SystemVerilog Design and Verification	FV	5	24-28 Sep	15-19 Oct	6-10 Aug, 12-16 Nov	29 Jul-02 Aug	
SystemVerilog Module-Based Verification: Plan-to-Closure Methodology	FV	1.5	3-4 Oct	On Request	16-17 Aug, 22-23 Nov	On Request	
Verification with VHDL	FV	4	24-27 Sep, 19-22 Nov	On Request	On Request	On Request	
Verification with PSL	FV	2	3-4 Dec	10-11 Sep, 3-4 Dec	12-13 Sep, 21-22 Nov	16-17 Jul	
Verilog for VHDL Engineers	FV	3	15-17 Oct	On Request	On Request	On Request	
Verilog Language & Applications	FV	5	12-16 Nov	24-28 Sep, 10-14 Dec	20-24 Aug, 8-12 Oct, 26-30 Nov	5-9 Aug	
VHDL Applications Workshop	FV	5	24-28 Sep, 3-7 Dec	17-21 Sep, 19-23 Nov	23-27 Jul, 17-21 Sep, 22-26 Oct, 10-14 Dec	On Request	
VHDL for Verilog Engineers	FV	3	17-19 Sep,	On Request		On Request	
Incisive Simulation	FV	2	8-9 Oct	13-14 Sept	10-11 Sept, 11-12 Oct, 3-4 Dec		
Specman Elite for Environment Users	FV	2	10-11 Dec	5-6 Nov	30-31 Jul, 24-25 Sep, 5-6 Nov, 17-18 Dec	12-13 Aug	
Specman Elite for Environment Developers	FV	3	12-14 Dec	7-9 Nov	1-3 Aug, 26-28 Sept, 7-9 Nov, 19-21 Dec		

DFM COURSES							
Class	Bu	Days	Bracknell,UK/ Kista, Sweden	Velizy, France	Munich, Germany	Herzelia, Israel	
Assura: Parasitic Extraction (RCX)	DFM	2	20-21 Sep	4-5 Oct, 20-21 Dec	12-13 Jul, 27-28 Sep, 25-26 Oct, 20-21 Dec	On Request	
Assura: RCX Developer	DFM	2	14-15 Aug	22-23 Oct	23-24 Aug	On Request	
Assura: Rules Writer (DRC/LVS Rules)	DFM	4	6-9 Oct	4-7 Sep	27-30 Aug, 3-6 Dec	15-18 Oct	
Electric.Verif.: VoltageStorm Power Rail Analysis	DFM	3	On Request	On Request	5-7 Sep, 28-30 Nov	22-24 Oct	
Assura Verification	DFM	3	17-19 Sept	1-3 Oct, 17-19 Dec	24-26 Sept, 22-24 Oct, 17-19 Dec	On Request	